Multi-Level Parallelism: A New Computing Technique

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High-performance computing platforms are evolving toward systems with ever larger numbers of processors. Linese systems linuite standard of the shelf microprocessors at the heart of their design. Virtually all hardware vendors have adopted this design approach because it dramatically reduces their costs. Unfortunately, this usually forces researchers to embark on large code-conversion efforts (often consuming work-years) to take advantage of the new parallel systems. Codes used in heavy production environments were often deemed imposting in about A workweeks. The initial nertormance of

Doing the zones in parallel is not new; the Message Passing Interface version of OVERFLOW arready attempts this process. The unique teature of the MLP approach is that it does so with no message passing and only a few hundred lines of code changes. This results in a code that is simple to maintain, that continues to execute well on C90 systems, and that now executes well on parallel systems at very high sustained levels of performance.

The MLP version of OVERFLOW was completed

occurred that are changing endors are now building of the hardware features of . In particular, the new ard true shared memory non-sers companoile writh structs. These new hardware ossible to approach parallely, a way that is more nplement.

the code for the 33-million-point problem was 75 percent that of the performance of the dedicated 16 CPU C90 system when executed on a 64 CPU Origin system. This amounts to 3.7 GFLOPS (10⁹ floating point operations per second). This relative performance coupled with the dramatically smallér price tor the Origin 2000 resulted in a price performance advantage of over 13.5. Further tests have demonstrated a sustained performance of over 6.3 GFLOPS for this problem on a 128 CPU Origin, with performance scaling still virtually linear at

sible to convert.

Several events have that attitude. First, the v systems that share many the Cray vector systems designs are moving tow arenhèctures, and fristruc vector programming cons attributes have made it pe ism in an entirely new wa intuitive and simpler to in

skacortenierbaienetiswen uwestw approaches to parallelism using the ics, Inc. Origin 2000 as a test bed. The new technique for parallel computing: rallelism (MLP). This project demonw parallel systems, such as the Origin e used in a heavy computational fluid D) production environment. Furthercodes, running on the Cray C90 d be converted to the parallel systems

tain their high-performance levels on chines. uction CFD code OVERFLOW was test bed for the MLP effort. s composed of approximately of FORTRAN. It was chosen because

ed one of the toughest tests for the resil-

stress-test the method, it was decided to execute only the largest and most complex problems. To this end a 33-million-point problem was selected. This problem was the largest that had ever been solved at Ames. It requires hundreds of C90 hours to converge.

mately 40% better than that of the dedicated C90. One of the most important features of the MLP technique is that the user can simply comment-out a few lines of code that call the MLP routines, and the code reverts back to the standard vector-friendly C90 version. This minimal change from the C90 version means that new releases of the OVERFLOW code can be converted rapidly to MLP. This was recently demonstrated with the introduction of OVERFLOW 1.8 which was converted to MLP form in less than

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The MLP_technique_has general applicability_to many CFD codes. MLP offers researchers the possibility of rapidly converting their Cray vector codes to the new machines, and of insuring high performance on each. Even more importantly, NASA researchers

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Amar Danar Bules ste gated these ne Silicon Graph end result is a multi-level pa strated that ne 2000, could b dynamics (CFI more, the CFT systems, couland still main the vector ma The prod chosen as the OVERFLOW_ 100,000 lines

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